SIMULATION OF HETERO-JUNCTION SILICON SOLAR CELLS

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Abstract: Solar modules based on single crystal and polycrystalline silicon have almost a 90% share of the total world market, primarily because of their stable, robust and reliable characteristics. For these reasons, a great deal of attention is devoted to improving their characteristics and especially their efficiency. One of the many approaches includes HIT solar cells (hetero-junction solar cell with an intrinsic thin amorphous layer) fabricated at temperatures below 300°C. In this paper, the electrical characteristics of HIT solar cells are simulated by using the TCAD Silvaco software package. The structure of HIT solar cell is optimized in order to obtain their maximum efficiency. In addition, the modifications of the simulation procedure, which allow the simulation of HIT solar cell, are described.

Keywords: solar cells, hetero-junction, amorphous/crystalline silicon, simulation.

1. INTRODUCTION

Mono-crystalline, poly-crystalline and amorphous-crystalline silicon solar cells are the most widely used group of commercial solar cells. This is primarily due to the fact that these solar cells have stable characteristics over a long period of time, good reliability during operation, and their mass production, as developed in the microelectronics technology, allows fast and relatively cheap production of solar cells based on silicon. The declining trend in the price of solar cells continues, and the technology improves so that the efficiency of solar cells produced becomes greater. One of the improvements is a combination of technologies based on single crystalline silicon (mono-crystalline silicon) layer with the addition of a thin amorphous layer. The resulting structure is known as HIT solar cell (hetero-junction with an amorphous intrinsic thin layer) produced by Sanyo Ltd., with the efficiency of commercially produced HIT solar cells over 20%, and with plenty of room for its improvement [1,2].

The production technologies of HIT solar cells use a thin intrinsic amorphous silicon layer (a-Si:H(i)) which is formed on both sides of wafer (Czochralski n-type c-Si), using low temperature PECVD process that takes place at 175°C or 250°C [3].In this way they avoid high temperature processes that degrade the interface surface of amorphous film and crystalline bulk. The HIT structure obtained in such a way has multiple advantages over the standard process. First, the width of a band gap Eg of amorphous silicon is in the range of 1.55eV to 1.87eV, which increases the efficiency at lower wavelengths, where the most concentrated intensity of terrestrial solar radiation can be found. Now, HIT structure shows better temperature characteristics, as well as a higher voltage V_{OC} (open-circuit voltage), because of a significant decrease in surface and interface recombination.

This paper presents the simulation results for the technology parameters and electrical characteristics of HIT solar cells. Program ATHENA [4] is used for the simulation of technological processes, while the electrical characteristics are simulated by program ATLAS [5]. ATHENA and ATLAS programs are an integral part of the Silvaco TCAD (Technology Computer-Aided Design) software package. At first, special attention was paid to the definition and analysis of the impact of interfacial and volume states on the characteristics of HIT solar cells, and then the structures of the HIT cells were electrically optimized, primarily in the V_{oc} and output power, taking into consideration the influence of thickness a-Si:H(p+) and the level of doping crystalline silicon substrate.

2. STRUCTURE OF HIT SOLAR CELL

The cross-section structure of HIT solar cell with a-Si:H(i) layer between the p+ doped amorphous silicon and c-Si(n+) from the front, and n+ doped amorphous silicon cathode and c-Si(n+) on the back side of solar cells is shown in Figure 1 [6]. The TCO (transparent conductive oxide) is placed on the front

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side (but it could be on both sides to provide a low serial resistance of cathode contact). An aluminum grid is placed on the upper surface, the first electrode, and the bottom surface is aluminum metallization as a second electrode. However, the standard HIT solar cells (Fig. 1b) do not have a-Si:H(i) which reduces the thickness of a-Si:H, but this layer increases the quantum efficiency of the structure, especially in smaller wave lengths, without significant impact on the value of voltage V_{oc}. The distribution of Impurity profile in the whole structure of HIT solar cell was obtained after simulation of the technology flow in ATHENA.
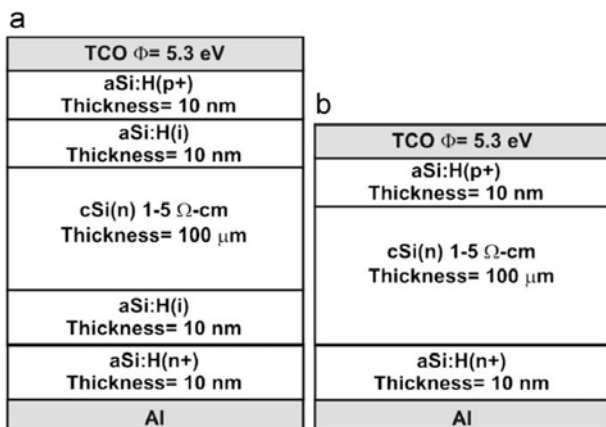

Figure 1. HIT solar cell structure: a) with the a-Si:H(i) layer and b) without this layer [6].

3. MODELS OF PHYSICAL PARAMETERS

To be able to simulate the electrical characteristics of HIT solar cells correctly and accurately, it is necessary to properly select and define the parameters of the physical models. This primarily refers to the definition of contacts, and to a model of the carriers generation and recombination, both in amorphous and mono-crystalline silicon, and the interfaces that exist in the structure.

In defining the anode contact (upper), it is necessary to carefully choose the material with its workfunction (WF), which needs to be higher than 5eV. Another parameter that significantly affects the characteristics is the level of doping c-Si. Lower resistance, that is, higher doping ($>$1E15cm^{-3}) increases the value of V_{OC}, until serial resistance R_s is activated, not only due to the presence of interface states (the electronic equivalent is a parallel resistance R_p), but also due to a width decrease of the depletion region, which reduces the absorption "surface" (the depletion region length).

An equivalent model is described in expression (1), where *I* and *U* are the output current and the output voltage of solar cells, respectively.

$$I = \frac{U}{R_P} + I_0 \left(e^{\frac{q(U+IR_s)}{nKT}} - 1 \right) - I_{SC} \qquad (1)$$

The influence of WF on the performance operates over Schottky-contact on the anode, i.e. contact potential, expression (2). This allows defining a barrier height at the metal-semiconductor interface φ_B, the expression (3).

$$\Psi_S = \chi + \frac{E_g}{2q} + \frac{kT_L}{2q} \ln\left(\frac{N_C}{N_V}\right) - WF + U \qquad (2)$$

$$WF = \chi + \phi_B \qquad (3)$$

where χ is the electron affinity (for amorphous Si is 3.8eV), T_L is the crystal lattice temperature, T_L=300K, N_C is the conduction band density of states of the amorphous Si (N_C(300K)=2.5E+20 cm^{-3}), N_V is the valence band density of states of the amorphous Si (N_V(300K)=2.5E+20 cm^{-3}), the width of bandgap of amorphous silicon is Eg = 1.72eV; WF and χ are given as a potential, while *U* is the voltage on the solar cell.

In defining a model of mobility in silicon, the already adopted values of Klaassen's mobility model are taken, while the value of amorphous silicon electron mobility is μ_e = 10cm^{2}V^{-1}s^{-1} and the holes mobility μ_h = 2cm^{2}V^{-1}s^{-1} (in n+ and p+ a-Si:H layers the same values are taken).

The recombination centers have a dominant influence on amorphous-crystalline silicon for which the model must include, in addition to the standard SRH, Auger's model and modified model of surface recombination, where it is necessary to define the acceptor-like states and donor-like states, both at the interface area a-Si/c-Si, and at the grain boundaries, the expression (4). In surface conditions, average carriers' lifetime τ_n and τ_p are taken into account, as a function of defect energy states, in the bandgap, and interface charge:

$$R_{SRH} = \frac{pn - n_{ie}^2}{\tau_p^{eff}\left[n + n_{ie}\exp\left(\frac{E-E_i}{kT_L}\right)\right] + \tau_n^{eff}\left[p + n_{ie}\exp\left(-\frac{E-E_i}{kT_L}\right)\right]}$$

$$(4)$$

The simulation of HIT structure using Silvaco TCAD software package requires defining the defects, as in a-Si: H and c-Si regions, and on the surface a-Si/c-Si. Gap-defect states distribution (or DOS - density of trap states) in a semiconductor is a parameter which models defects in recombination equations. Due to the nature of amorphous-crystalline silicon their density was significantly increased in comparison to structures produced by standard technology. It is understood that the donor-like NTD and acceptor-like NTA states are presented. Howe-

ver, the profile of energy distribution can be taken as a standard "U" profile by defining the two functions for acceptor and donor cover centers, respectively: exponential "tail" function (Urbach's position) by the end of the zone, and the Gaussian function in the middle of the zone (Gaussian mid-gap states associated to silicon dangling bonds). The total density of states, shown in Figure 2, would then be:

$$g(E) = g_{TA}(E) + g_{TD}(E) + g_{GA}(E) + g_{GD}(E) \quad (5)$$

Exponential band "tail" defined for acceptor-like states and donor-like states are:

$$g_{TA}(E) = NTA \exp\left(\frac{E - E_C}{WTA}\right) \quad (6)$$

$$g_{TD}(E) = NTD \exp\left(\frac{E_V - E}{WTD}\right) \quad (7)$$

NTA and NTD are the maximum of acceptor-like and donor-like trap densities (band tail density of states), respectively. They are equal in their values (NTA = NTD = 2E+21cm⁻³eV⁻¹). E_C is the conduction band energy, E_V is the valence band energy, WTA and WTD are characteristic decay energy for acceptors and donors (WTA=0.03eV and WTD=0.06eV). The mid-gap traps described by Gaussian distributions of donor-like states and acceptor-like states are:

$$g_{GD}(E) = NGD \exp\left[-\left(\frac{E - EGD}{WGD}\right)^2\right] \quad (8)$$

$$g_{GA}(E) = NGA \exp\left[-\left(\frac{EGA - E}{WGA}\right)^2\right] \quad (9)$$

NGD and NGA are Gaussian maximum energy density of states in the doped layer (NGD=8E+17cm⁻³, NGA=5E+20cm⁻³), while for the intrinsic layer the following applies: NGD=8E+15cm⁻³ and NGA=5E+17cm⁻³. EGD and EGA are the Gaussian peak energies, EGD=1.22eV and EGA=0.7eV. The standard deviation for these functions is: WGD=WGA=0.23eV.

Capture cross-sections for the exponential "tail" distributions are: SIGTAE=1E-17, SIGTAH=1E-15 for the acceptor traps, and SIGTDE=1E-15, SIGTDH=1E-17 for the donor traps. Capture cross-section for acceptor-like and donor-like states of Gaussian levels are SIGGAE=1E-15, SIGGAH=1E-14, SIGGDE=1E-14 and SIGGDH=1E-15, respectively. These values are retained and are the same for c-Si, and also in the description of the interface a-Si/c-Si. The interface surface concentration of interface charge is QF=1E+10cm⁻², while the surface recombination velocity of electrons and holes are S.N=S.P=1E+3cm/s. Modeling was carried out for

the life-time of carriers on the interface surface a-Si/c-Si, which is described in equations for the electrons and holes lifetimes:

$$\frac{1}{\tau_n^{eff}} = \frac{1}{\tau_n^i} + \frac{d_i}{A_i} S.N \quad (10)$$

$$\frac{1}{\tau_p^{eff}} = \frac{1}{\tau_p^i} + \frac{d_i}{A_i} S.P \quad (11)$$

where τ^i time specified for bulk of semiconductors, and A_i and D_i are the length and area of influence for each analyzed point.
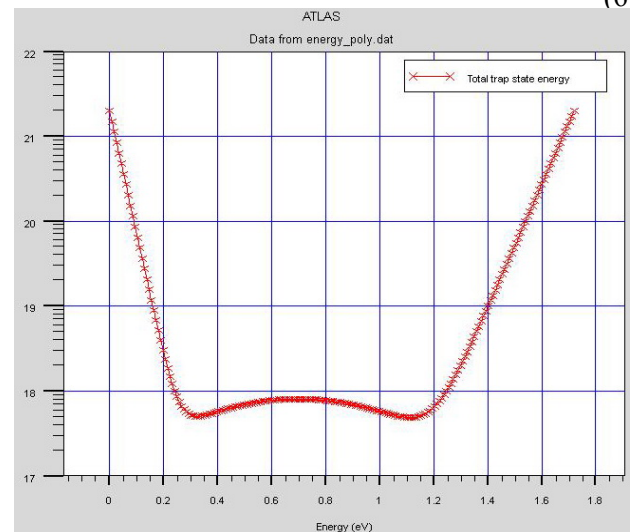
(6)

Figure 2. The total distribution of energy states g(E) of amorphous silicon

3. SIMULATION RESULTS AND DISSCUSION

The simulated HIT solar cell has two intrinsic amorphous layers on both sides of mono-crystal silicon, where both of them are 10nm thick. The bulk itself is an n-type phosphor-doped, the concentration is from 3E+15cm⁻³ to 1E+17cm⁻³. The doped amorphous top layer is also a 10nm thick, doped with boron, doping concentration is 3E+19cm⁻³. The results showed that this concentration is a limit value for which the efficiency of solar cells enters saturation. The lower layer a-Si:H(n+) is doped with phosphorus, the concentration is 1E+19cm⁻³. Anode electrode is a TCO-type, 80nm thick, while the cathode is made of Al. During the simulation the possible reflection of light from the lower electrode is considered. For simulation under illumination the standard AM 1.5 normalized to 0.1W/cm² or 100mWcm⁻² (1sun) was used, the wavelength varies from 0.25μm to 2.4μm. In this way the excitation of solar insulation for terrestrial systems is positioned.

As already noted, the value of the workfunction for TCO electrodes affects the open circuit voltage V_{OC} and thus the efficiency of solar cells. It is therefore important to perform the analysis of its influence. We take the values 4.9eV, 5.1eV, 5.3eV and 5.5eV. The results of output simulated characteristics are shown in Figure 3, while Figure 4 shows the power that is distributed to the output contacts.

Therefore, for the values of WF less than 5.3eV, the voltage V_{OC} is significantly changeable and declining, while for larger values it saturates; the maximum value of V_{OC}=0.6V is obtained for the WF=5.5eV. For example, when the simulation uses the level of doping 1E+16 and WF=5.5eV, we obtain the efficiency η=22%. It should be noted that the actual processes can make the TCO electrode with the indicated value of WF.

The next parameter that proved to be very important is the level of doping mono-crystalline silicon during the CZ process. The influence of this concentration is two-fold, at least taking into account the basic parameters. The first is the serial resistance reduction until the mobility of carriers is significantly reduced, increasing the V_{OC}, and the second is the influence on the width of the depletion region. As a side effect the relationship between the impurity concentration and interface traps could be observed. In Figure 5 V_{OC} saturation is noticed, when the doping is greater than 1E+17cm^{-3}, and the decrease of I_{SC} with increasing the substrate doping can also be observed (Figure 6). Figure 7 shows the dependence of output power, where the nominal voltage for a given level of doping can be read. In addition, in order to determine the coefficient of efficiency in % it is only necessary to multiply the maximum value from the graphics to 10^{12} (take the absolute value). Besides the saturation of the V_{OC} there is also the saturation in the HIT solar cell efficiency η. Therefore, for optimum doping value can be 7E+16cm^{-3}, which gives V_{OC}=0.64V, η=23.6% and FF=83.48%. With further doping, increased voltage slowly begins to decline, while the short circuit current starts to decrease significantly.

When it comes to optimizing the thickness of amorphous silicon layer, the simulations are carried out for the following thicknesses: 10nm, 20 nm, 30 nm, 40nm and 50nm, with the same thickness for p+, n+ and intrinsic amorphous silicon. From the common graphic of all 5 structures (Figure 8), it is difficult to see the difference between I-V characteristics. The changing of current I_{SC} is clearly evident in Figure 9 while the dependence of change in voltage V_{OC} is shown in Figure 10. Thus, increasing the thickness of layers of amorphous silicon does not result in the substantial increase in I_{SC} and V_{OC}. This suggests that the previously defined values for the WF and doping for mono-crystalline silicon can be kept, for the a-Si:H thickness as 10nm for all layers, which would save both material and energy, which would ultimately impact reducing the price of HIT solar cells.
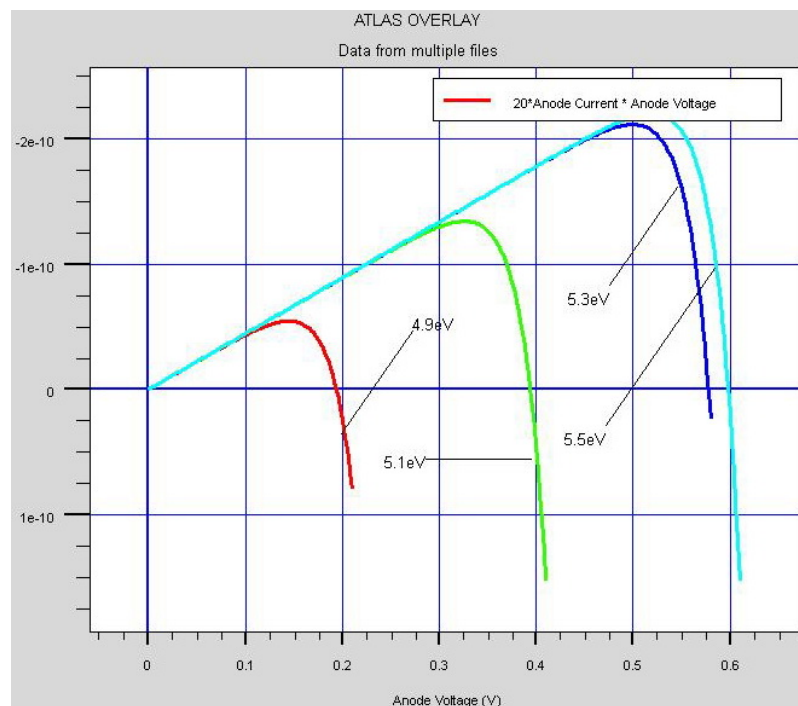

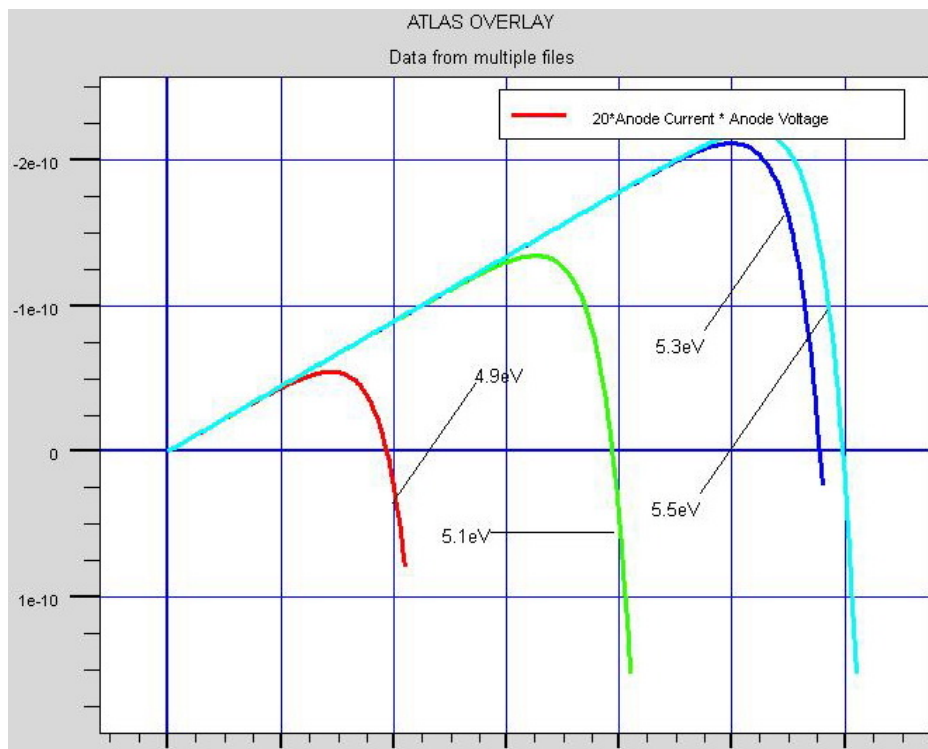*Figure 3. I-V characteristics of HIT solar cells for different values of the parameter WF*

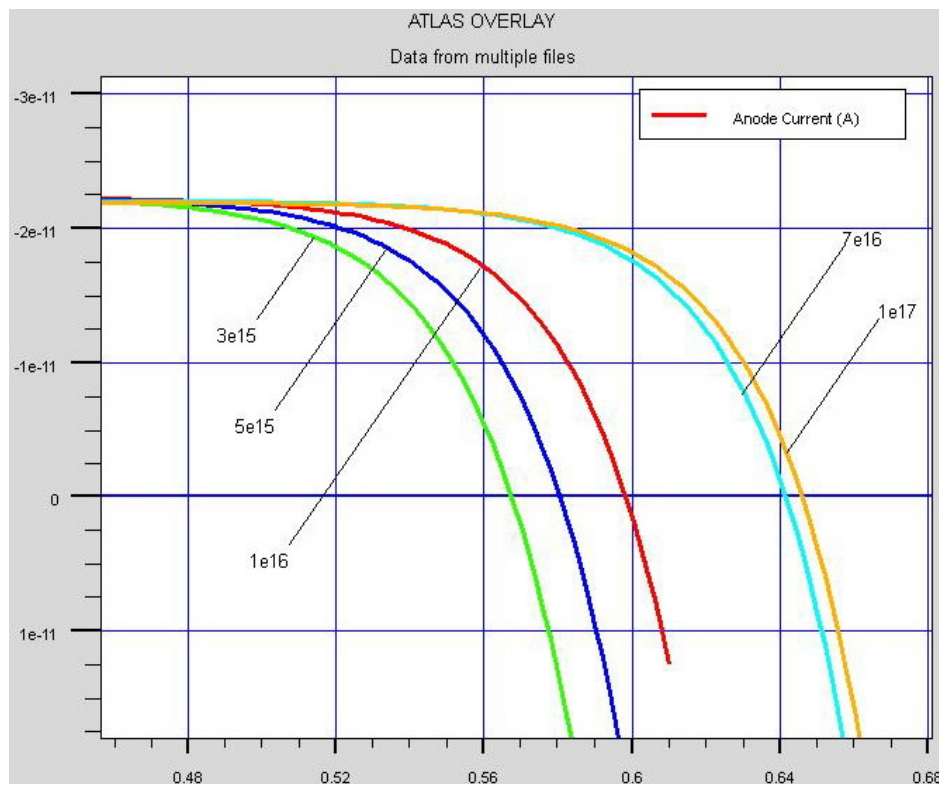*Figure 4. Output power per μm² for different values of the parameter WF*

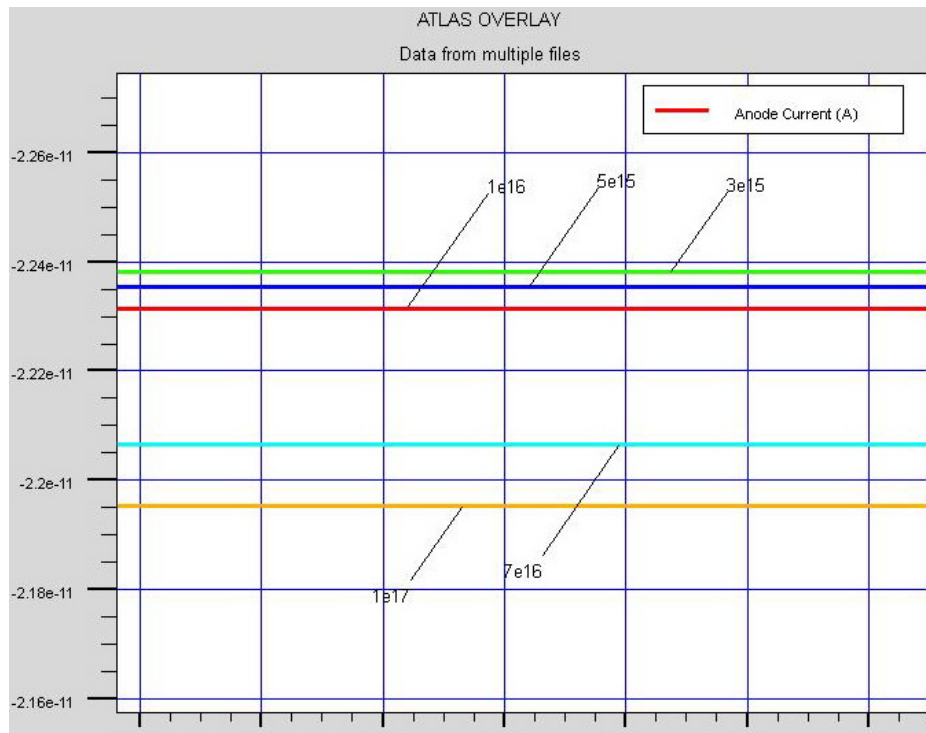*Figure 5. Dependence of V_{oc} from doping of c-Si*

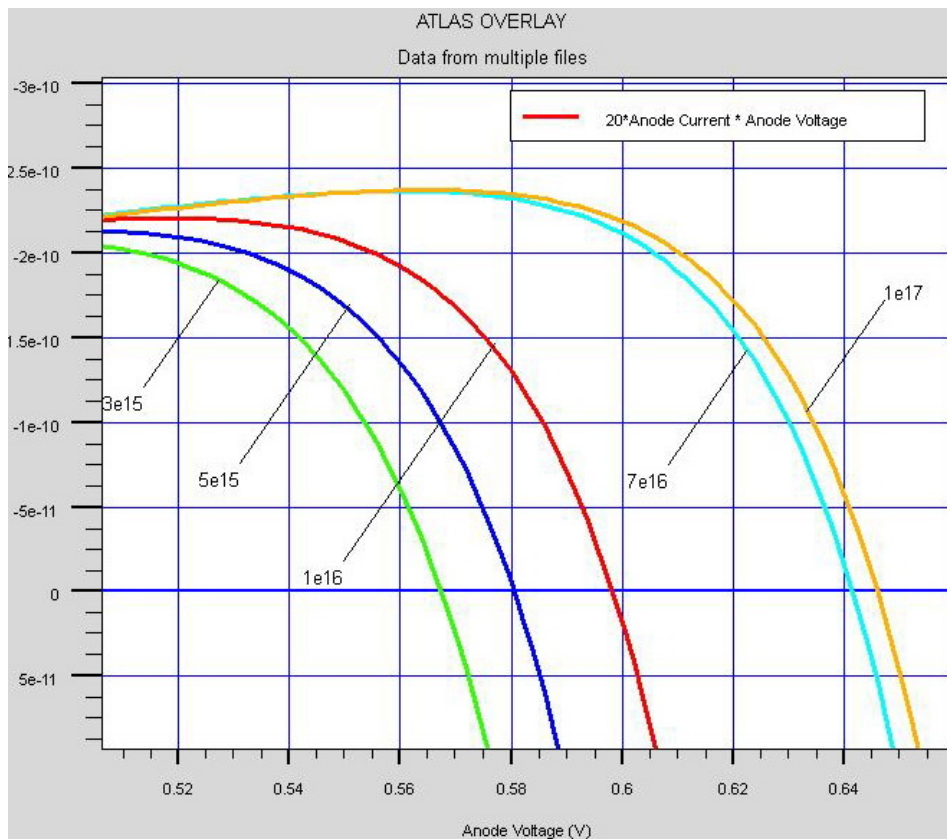*Figure 6. Dependence of I$_{sc}$ from doping of c-Si*

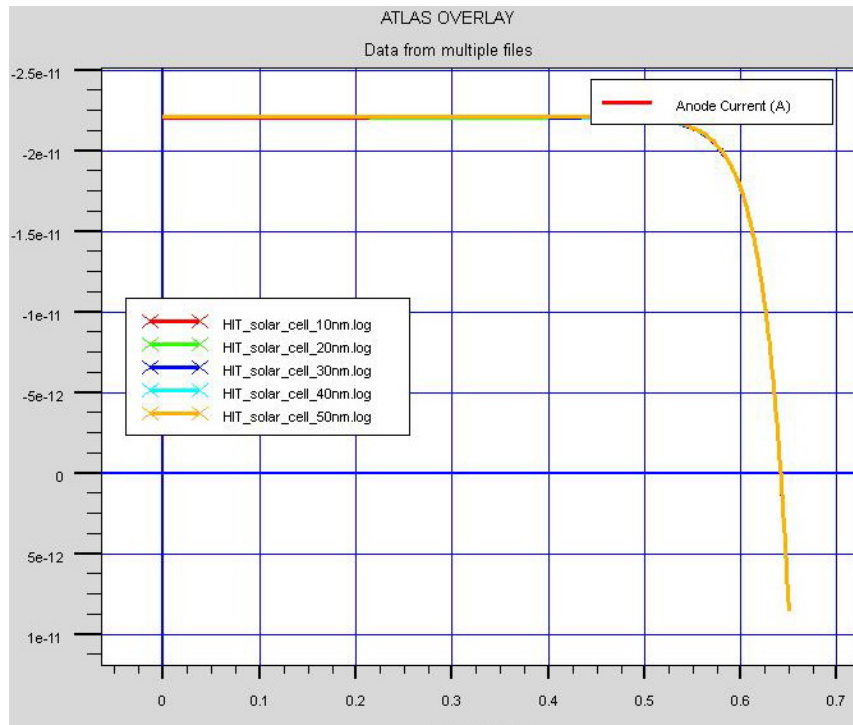*Figure 7. Dependence of P (W/μm^2) on doping of c-Si*

Figure 8. I-V characteristics of structures with variable thickness of a-Si
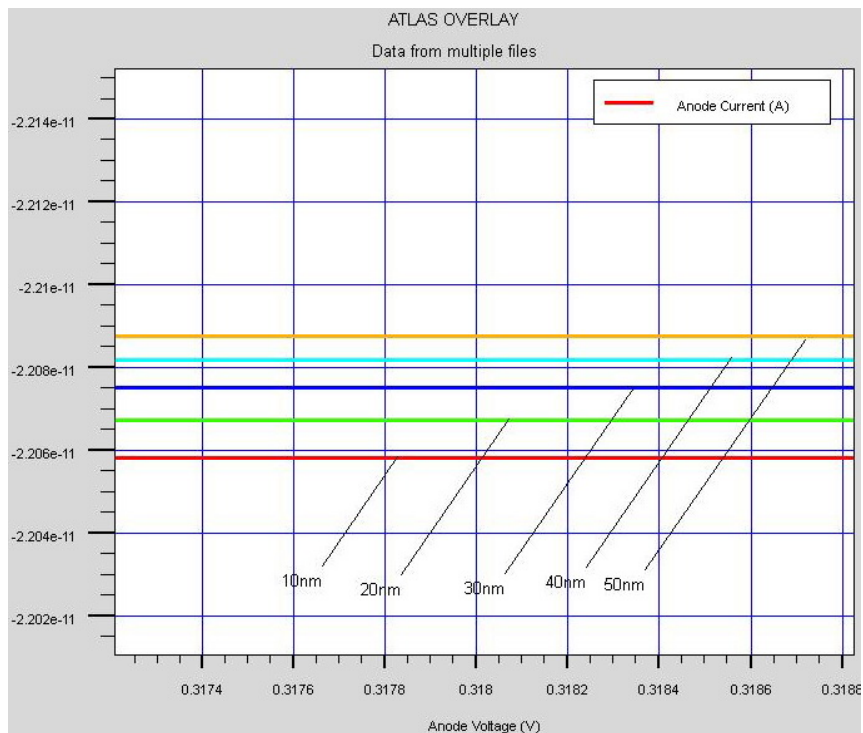

Figure 9. Dependence of I_{SC} for structures with variable thickness of a-Si
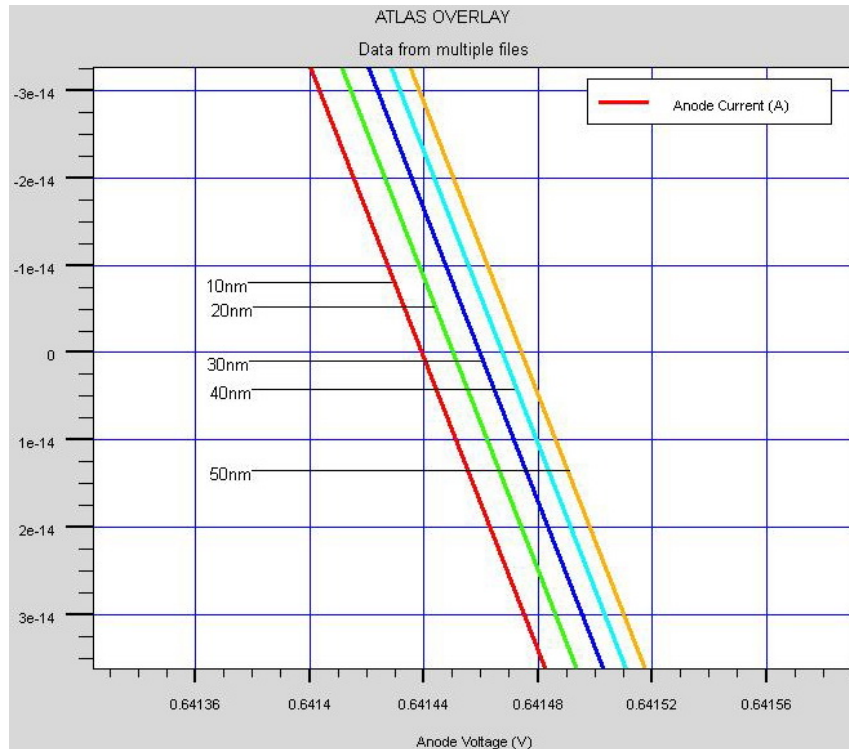
Figure 10. Dependence of V_{OC} for structures with variable thickness of a-Si

4. CONCLUSION

This work presents the results of simulation and optimization of the technology flow, and electrical characteristics of HIT solar cells. For the simulation of technological processes of production, ATHENA software was used, while the electrical characteristics were simulated in ATLAS software. ATHENA and ATLAS programs are an integral part of the Silvaco TCAD (Technology Computer-Aided Design) software package. It is shown that the proper choices of physical model parameters are: mobility, generation and recombination, as well as defining the contacts, and the interface can successfully and accurately simulate the electrical characteristics of HIT solar cells. The structure with the best characteristics in terms of V_{OC} and efficiency η was obtained by optimizing the most important parameters of HIT structure: the level of doping and thickness of layers.

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СИМУЛАЦИЈА ЕЛЕКТРИЧНИХ КАРАКТЕРИСТИКА ХЕТЕРОСЛОЈНИХ СОЛАРНИХ ЋЕЛИЈА

Сажетак: Соларни модули који се базирају на монокристалном и поликристалном силицијуму представљају скоро 90% укупног светског тржишта, пре свега због својих стабилних, робусних и поузданих карактеристика у поређењу са соларним ћелијама које се производе новим технологијама и које пре свега налазе примену у свемирским апликацијама. Из ових разлога се велика пажња посвећује побољшању њихових карактеристика, пре свега њихове ефикасности. Један од многобројних приступа је развој соларних ћелија које се производе на температурама нижим од 300°C. Ове соларне ћелије су познате као ХИТ соларне ћелије – хетерослојне соларне ћелије са танким интринсичним слојем аморфног силицијума. У овом раду су карактеристике ХИТ соларних ћелија симулиране програмом Silvaco, при чему је њихова структура оптимизована са циљем да се добије њихова максимална ефикасност. Поред тога, описане су и модификације у процедури симулације које омогућавају симулацију ХИТ соларних ћелија, с обзиром на то да програм не узима у обзир високе концентрације међуповршинских густина стања, а које су карактеристичне за хетероспојеве аморфног и кристалног силицијума.

Кључне речи: соларне ћелије, хетероспој, аморфни силицијум, монокристални силицијум, симулација.

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